IDC Plug

Female DSUB

GND

Data

To J3 on TDS400 CPU board

10 9
8 7
6 5
4 3
2 1

1
6
2
7
3
8
4
9
5

To serial port on PC with Terminal Program

Note that pin numbers refer to
connector numbers on the board
and not to arrows on both IDC
header and plug which are 180
deg from this referenced to the key slot.

```
        Bootrom Total Checksum passed.
        BootRom Check Sum passed.
        Bus Error Timeout test passed.
        Bus Error Write to Bootrom passed.
        GPIB Test passed.
Kernel Diagnostics Complete.

Calling SDM (monitor) Routine.

SDM (monitor) not enabled.
        Enabling Bus Control register. Value = 0x2
        Flashrom Programming Voltage is OFF.
        Flashrom DSACK and JumpCode test passed.
        Flashrom Checksums passed.
Bootrom Diagnostics Complete.

Transferring control to the Flashrom.
sysDramControllerInit
sysDramByteStrobeTest
sysDramTest
bcopy(<Idata>)
bzero(<bss>)
intVecBaseSet(getVbr())
sysSymTbl=standAloneSymTbl
sysDevDramTest
0x0 bytes of development dram found
validateDataSpace
Outer Kernel DSACK Test
Pending Interrupt Test
Walk IPL to Zero Test
Timer Int Test
Start Power-On Diag Sequence
hwAccountant probe routines
  Probe for unexpected pending ints
  Dsp Instr mem size
  Dsp D2 mem size
  Dsp D1 mem size
  Dsy Vect0 mem size
  Dsy Vect1 mem size
  Dsy Wfm0 mem size
  Dsy Wfm1 mem size
  Dsy Text0 mem size
  Dsy Text1 mem size
  Acq number of digitizers
  Acq mem size
  Cpu timer interval uSec
  Cpu Dram size
  NvRam mem size
  Opt RS232/ Cent presence
  Acq 8051 presence
  Acq ADG209C presence
  Acq record length size
dsyWaitClock ................... pass
clockCalVerify ................ pass
```

```
cpuDiagBatTest ................. pass
cpuDiagAllInts ................. pass
cpuEEromLibDiag ............... pass
calLibDefaultCk ............... pass
dspForcedBus ................... pass
dsp68kD2MemTest ............... pass
optRS232DuartIO ............... UNTESTED
dsp68kMemTest .................. pass
dspRunVerify ................... pass
dspBusRequestTest ............. pass
dspImplicitBusAccess .......... pass
dspTristarMemTest ............. pass
dspDsyToDspInts ............... pass
dspAcqToDspInts ............... pass
nvLibrariansDiag .............. pass
dsyDiagResetReg ............... pass
atBusTest ..................... pass
dsyDiagResetReg ............... pass
dsyDiagVscReg ................. pass
dsyDiagPPRegMem ............... pass
dsyDiagRasRegMem .............. pass
dsyDiagRegSelect .............. pass
dsyDiagRamdacRegMem ........... pass
dsyDiagAllMem ................. pass
dsySeqYTModeV0Intens .......... pass
dsyDiagSeqXYModeV1 ............ pass
dsyRastModeV0Walk ............. pass
dsyRastModeV1Attrib ........... pass
dsyWaitClock .................. pass
attn8051testResult ............ pass
attnDACReadback ............... pass
acq8051testResult ............. pass
adgRegDiag .................... pass
dsyWaitClock .................. pass
adgMemTestDiag ................ pass
trigComparatorTest ............ pass
trigDBERunsAfter .............. pass
tbiRampTest ................... pass
acqRampDiagAll ................ pass
dsyWaitClock .................. pass
fpDiagConf .................... pass
$Header: scopeSTR.version,v 1.10 94/06/23 17:08:17 aland Exp $
->
Smalltalk/V Sun Version 1.12
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```